

A characterization of Polynomial Space with Forks

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Outline

CPSF

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Semantics

Strong normalization, lock-freedom and confluence

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ICC and polynomial Space

ICC related works on polynomial space :

- ▶ Function algebra with parameter substitution (Leivant-Marion 94)
- ▶ Function algebra with ramified recurrence (Leivant-Marion 97)
- ▶ Quasi-interpretation with LPO (Bonfante-Marion-Moyen 07)
- ▶ Lambda calculus with LLL based type system (Gaboardi-Ronchi Della Rocca-Marion 07)
- ▶ Higher-order types or life without cons (Jones 01)
- ▶ Matrix calculus (Niggli-Wunderlich, Jones-Kristiansen, Moyen)

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Our approach

Take the methodology of the type system presented in LICS 2011 (Marion) that combines :

- ▶ data ramification principle (or tiering)
- ▶ with non-interference based type system
- ▶ on a simple imperative language

in order to characterize polynomial space on an imperative language with wait/fork mechanism.

Advantages of the presented methodology :

- ▶ a good expressivity
- ▶ very close to C-fork processes

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Simple While Language with fork/wait

$$E, E_1, \dots, E_n \in \text{Exp} \quad ::= \quad X \mid \mathbf{op}(E_1, \dots, E_n)$$

$$I \in \text{Inst} \quad ::= \quad \text{fork}() \mid \text{wait}(E)$$

$$\begin{aligned} C, C' \in \text{Cmd} \quad ::= \quad & X := E \mid C; C' \mid \text{skip} \\ & \mid \text{while}(E) \text{do}\{C\} \\ & X := I \mid \text{if } E \text{ then } C \text{ else } C' \end{aligned}$$

$$P \in \text{Proc} \quad ::= \quad \text{return } X \mid C; P$$

$$X \in \mathcal{X} \text{ and } \mathbf{op} \in \mathbb{O}$$

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Main results

A type system for imperative programs such that :

- ▶ Typable programs are computable in polynomial space under some restrictions :
 - ▶ termination
 - ▶ confluence
 - ▶ lock-freedom
 - ▶ the return type
- ▶ Each polynomial space problem can be computed by a typed program
- ▶ In a terminating program, all processes compute in polynomial time (polynomial number of steps)

A process being either the main program process or a subprocess created by a fork instruction.

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Informal semantics : store, configuration and environment

Let \mathbb{W} be the set of words over Σ .

Sequential commands are evaluated as usual

A process P is evaluated inside a configuration $c = (P, \mu)_\rho$:

- ▶ P is the program counter
- ▶ A store $\mu : \mathcal{X} \rightarrow \mathbb{W}$ mapping each variable of P to a value
- ▶ A set of ids ρ , the sons of c
- ▶ each configuration has an id (an integer). The main process id is 1.

All configurations are stored in an environment \mathcal{E} , a partial function, mapping an id $\in \mathbb{N}$ to a configuration c .

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Informal semantics : fork

At the beginning, there is only one configuration (the main process) of id 1 and with $\rho = \emptyset$.

A fork instruction creates a new child :

- ▶ with a new id (set to the next available integer)
- ▶ that runs concurrently of its father
- ▶ with its own duplicated memory (the store and the program counter are duplicated)
- ▶ the child id is stored in the father ρ

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Informal semantics : wait

The wait instruction provides a *one-way* communication and is the only way for a father to communicate with its child.

A *wait*(E) instruction :

- ▶ evaluates the expression E to a binary number n encoding id n
- ▶ if $n \in \rho$ and the child is *returning* then :
 - ▶ the child return value is passed to the father
 - ▶ the child is erased
- ▶ otherwise the father waits for its children

Note that children of a killed father may still be alive

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Example

```

P:   X := fork ();
Q:   if X > 0 then {
R:       Y := wait(X);
          Y := Y + 1
        } else {
          Y := 17
        }
S:   return Y

```

Initial environment :

$$\mathcal{E}(1) = (P, \mu)_{\emptyset}$$

Fork evaluation :

$$\mathcal{E}(1) = (Q, \mu\{X := \underline{2}\})_{\{2\}} \quad \mathcal{E}(2) = (Q, \mu\{X := \underline{0}\})_{\emptyset}$$

After some steps :

$$\mathcal{E}(1) = (R, \mu\{X := \underline{2}\})_{\{2\}} \quad \mathcal{E}(2) = (S, \mu\{X := \underline{0}, Y := \underline{17}\})_{\emptyset}$$

Wait evaluation :

$$\mathcal{E}(1) = (S, \mu\{X := \underline{2}, Y := \underline{18}\})_{\{2\}} \quad \mathcal{E}(2) = \perp$$

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Semantics of expressions and configurations

$$(X, \mu) \xrightarrow{e} \mu(X)$$

$$(\mathbf{op}(E_1, \dots, E_n), \mu) \xrightarrow{e} \llbracket \mathbf{op} \rrbracket(d_1, \dots, d_n)$$

if $\forall i, (E_i, \mu) \xrightarrow{e} d_i$

$$(\text{skip}; P, \mu)_\rho \xrightarrow{c} (P, \mu)_\rho$$

$$(X := E; P, \mu)_\rho \xrightarrow{c} (P, \mu\{X \leftarrow d\})_\rho$$

if $(E, \mu) \xrightarrow{e} d$

$$(\text{if } E \text{ then } C_{\text{tt}} \text{ else } C_{\text{ff}}; P, \mu)_\rho \xrightarrow{c} (C_w; P, \mu)_\rho$$

if $(E, \mu) \xrightarrow{e} w \in \{\text{tt}, \text{ff}\}$

$$(\text{while}(E)\text{do}\{C\}; P, \mu)_\rho \xrightarrow{c} (P, \mu)_\rho$$

if $(E, \mu) \xrightarrow{e} \text{ff}$

$$(\text{while}(E)\text{do}\{C\}; P, \mu)_\rho \xrightarrow{c} (C; \text{while}(E)\text{do}\{C\}; P, \mu)_\rho$$

if $(E, \mu) \xrightarrow{e} \text{tt}$

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Semantics of environments

Let $\mathcal{E}' = \mathcal{E}[i := c]$ be defined by :

- ▶ $\mathcal{E}'(j) = \mathcal{E}(j), \forall j \in \text{dom}(\mathcal{E}) - \{i\},$
- ▶ $\mathcal{E}'(i) = c$

The transition \rightarrow for process evaluation is defined by :

$$\begin{aligned} \mathcal{E}[i := c] &\rightarrow \mathcal{E}[i := c'] \\ &\text{if } c \xrightarrow{c} c' \end{aligned}$$

$$\begin{aligned} &\mathcal{E}[i := (X := \text{fork}()); P, \mu]_{\rho} \\ &\rightarrow \mathcal{E}[i := (P, \mu\{X \leftarrow \underline{n}\})_{\rho \cup \{n\}}, n := (P, \mu\{X \leftarrow \underline{0}\})_{\emptyset}] \\ &\text{with } n = \# \mathcal{E} + 1 \end{aligned}$$

$$\begin{aligned} &\mathcal{E}[i := (X := \text{wait}(E)); P, \mu]_{\rho} \\ &\rightarrow \mathcal{E}[i := (P, \mu\{X \leftarrow \mu'(Y)\})_{\rho}, n := \perp] \\ &\text{if } (E, \mu) \xrightarrow{e} \underline{n}, n \in \rho \text{ and } \mathcal{E}_n = (\text{return } Y, \mu') \end{aligned}$$

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Strong normalization

A process P is strongly normalizing if $\forall \mu$ there is no infinite reduction starting from $(P, \mu)_\emptyset$ through the relation \rightarrow .

Lock-freedom

If $\mathcal{E} \not\rightarrow$ and $\mathcal{E}_1 = (X := \text{wait}(E); P, \mu)_\rho$ then \mathcal{E} is locked.

A process P is *lock-free* if $\forall \mu$, there is no locked environment \mathcal{E}' s.t. $(P, \mu)_\emptyset \xrightarrow{*} \mathcal{E}'$.

Confluence

A process P is *confluent* if $\forall \mu$, $(P, \mu)_\emptyset \xrightarrow{*} \mathcal{E}'$ and $(P, \mu)_\emptyset \xrightarrow{*} \mathcal{E}''$, $\exists \mathcal{E}^3$ s.t. $\mathcal{E}' \xrightarrow{*} \mathcal{E}^3$ and $\mathcal{E}'' \xrightarrow{*} \mathcal{E}^3$.

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Example of non-confluent process

```
P: X := fork ();  
   Y := fork ();  
   return Y
```

The main process will return the process identifier of its second son.

Depending on the order in which execution of the subprocesses occurs, this identifier can be either 3 or 4.

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Tiers and typing environments

- ▶ *Tiers* are two elements $\mathbf{0}, \mathbf{1}$ underlying a boolean lattice $(\{\mathbf{0}, \mathbf{1}\}, \preceq, \mathbf{0}, \vee, \wedge)$ such that $\mathbf{0} \preceq \mathbf{1}$
- ▶ Operator types τ are defined by

$$\tau ::= \alpha \mid \alpha \longrightarrow \tau, \alpha \in \{\mathbf{0}, \mathbf{1}\}$$

- ▶ A *variable typing environment* Γ maps each variable in \mathcal{V} to a tier in $\{\mathbf{0}, \mathbf{1}\}$
- ▶ An *operator typing environment* Δ maps each operator \mathbf{op} of arity n to a set $\Delta(\mathbf{op})$ of operator types of the shape $\tau = \alpha_1 \longrightarrow \dots \alpha_n \longrightarrow \alpha$

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$$\frac{\Gamma(X) = \alpha}{\Gamma, \Delta \vdash X : \alpha} \text{ (EV)}$$

$$\frac{\Gamma, \Delta \vdash E_j : \alpha_j \quad \alpha_1 \longrightarrow \dots \longrightarrow \alpha_n \longrightarrow \alpha \in \Delta(\mathbf{op})}{\Gamma, \Delta \vdash \mathbf{op}(E_1, \dots, E_n) : \alpha} \text{ (EO)}$$

$$\frac{\Gamma, \Delta \vdash \mathbf{op}(E_1, \dots, E_n) : \alpha \quad \Gamma, \Delta \vdash X : \mathbf{0}}{\Gamma, \Delta \vdash_\beta X := \mathbf{fork}() : \mathbf{0}} \text{ (F)}$$

$$\frac{\Gamma, \Delta \vdash E : \mathbf{0} \quad \Gamma, \Delta \vdash X : \alpha}{\Gamma, \Delta \vdash_\beta X := \mathbf{wait}(E) : \alpha} \alpha \preceq \beta \text{ (W)}$$

$$\frac{\Gamma, \Delta \vdash X : \alpha \quad \Gamma, \Delta \vdash E : \alpha' \quad E \in \mathbf{Exp}}{\Gamma, \Delta \vdash_\beta X := E : \alpha} \alpha \preceq \alpha' \text{ (CA)}$$

$$\frac{\Gamma, \Delta \vdash_\beta C : \alpha \quad \Gamma, \Delta \vdash_\beta C' : \alpha'}{\Gamma, \Delta \vdash_\beta C; C' : \alpha \vee \alpha'} \text{ (CC)}$$

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$$\frac{\Gamma, \Delta \vdash E : \mathbf{1} \quad \Gamma, \Delta \vdash_{\beta} C : \alpha}{\Gamma, \Delta \vdash_{\beta} \text{while}(E)\text{do}\{C\} : \mathbf{1}} \text{ (CW)}$$

$$\frac{\Gamma, \Delta \vdash E : \alpha \quad \Gamma, \Delta \vdash_{\beta} C : \alpha \quad \Gamma, \Delta \vdash_{\beta} C' : \alpha}{\Gamma, \Delta \vdash_{\beta} \text{if } E \text{ then } C \text{ else } C' : \alpha} \text{ (CB)}$$

$$\frac{}{\Gamma, \Delta \vdash_{\beta} \text{skip} : \alpha} \text{ (CS)}$$

$$\frac{\Gamma, \Delta \vdash_{\beta} C : \mathbf{0}}{\Gamma, \Delta \vdash_{\beta} C : \mathbf{1}} \text{ (CSub)}$$

$$\frac{\Gamma, \Delta \vdash_{\beta} C : \alpha \quad \Gamma, \Delta \vdash X : \beta}{\Gamma, \Delta \vdash C; \text{return } X : \beta} \text{ (P)}$$

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Some more intuitions

- ▶ The type discipline precludes values from flowing from tier **0** to **1** (but not command because of CS)
- ▶ Consequently, while loop guards are enforced to be of tier **1** (CW)
- ▶ In a (CB) rule the guard tier is equal to tier of both branches (could be weakened)
- ▶ However information may flow in the opposite direction (CA)
- ▶ The annotation β keeps track of the return type and is used by wait instructions (W)

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Example :

```

found0 := ff0 : 0 ;
n1 := length(str)1 : 1;
l1 := n/21 : 1;
x0 := fork()0 : 0;
while l>01 do {
  if x>00 then
    c0 := getchar(str1, l1)1 : 0;
  else
    c0 := getchar(str1, n-l1)1 : 0;
    if c=='*'0 then
      found0 := tt0 : 0
    else skip : 0 ;
    l1 := l-11 : 1
  } : 1
if x>00 then {
  sonf0 := wait(x)0 : 0;
  found0 := or(found, sonf)0 : 0
} else skip : 0 ;
return found0

```

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Neutral, positive and polynomial operators

An operator **op** is :

1. *neutral* ($\mathbf{op} \in Ntr$) if :

1.1 either $\forall n \in \mathbb{N}^*$, $\llbracket \mathbf{op} \rrbracket(\vec{W}_n) \subseteq \mathbb{W}_{n-1}$.

1.2 or there is a polynomial $P_{\mathbf{op}}$ s.t. $\forall n \in \mathbb{N}^*$, $\exists \mathbb{V}_n^{\mathbf{op}} \subseteq \mathbb{W}_n$,

$$\llbracket \mathbf{op} \rrbracket(\vec{W}_n) \subseteq \mathbb{V}_n^{\mathbf{op}} \text{ and } \#\mathbb{V}_n^{\mathbf{op}} \leq P_{\mathbf{op}}(n)$$

2. *positive* ($\mathbf{op} \in Pos$) if $\mathbf{op} \notin Ntr$ and there is $c_{\mathbf{op}} \in \mathbb{N}$ s.t. :

$$\forall \vec{d} \in \mathbb{W}^m, |\llbracket \mathbf{op} \rrbracket(\vec{d})| \leq \max_{i \in [1, m]} |d_i| + c_{\mathbf{op}}$$

3. *polynomial* ($\mathbf{op} \in Pol$) if $\mathbf{op} \notin Ntr \cup Pos$ and there is a polynomial $Q_{\mathbf{op}}$ s.t. :

$$\forall \vec{d} \in \mathbb{W}^m, |\llbracket \mathbf{op} \rrbracket(\vec{d})| \leq Q_{\mathbf{op}}(\max_{i \in [1, m]} |d_i|)$$

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Safe operator typing environment

Definition

Δ is *safe* if $\forall \mathbf{op} \in \text{dom}(\Delta)$ and $\forall \alpha_1 \rightarrow \dots \rightarrow \alpha_n \rightarrow \alpha \in \Delta(\mathbf{op})$ we have :

- ▶ if $\mathbf{op} \in \text{Ntr}$ then $\alpha \preceq \bigwedge_{i=1, n} \alpha_i$,
- ▶ if $\mathbf{op} \in \text{Pos}$ then $\alpha = \mathbf{0}$,
- ▶ if $\mathbf{op} \in \text{Pol}$ then $\forall i \in [1, n]$, $\alpha_i = \mathbf{1}$ and $\alpha = \mathbf{0}$

Intuitively :

- ▶ Neutral operators are iterable ($\mathbf{1} \rightarrow \mathbf{1}$ in a while loop guard).
- ▶ Positive operators are not iterable but composable ($\mathbf{0} \rightarrow \mathbf{0}$ in a while-loop command).
- ▶ Polynomial operators are neither iterable nor composable ($\mathbf{1} \rightarrow \mathbf{0}$).

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Examples

$$\begin{aligned}
 (\text{Ntr 1.1}) \quad \llbracket \text{pred} \rrbracket(u) &= \varepsilon && \text{if } u = \varepsilon \\
 &= w && \text{if } u = a.w
 \end{aligned}$$

$$\begin{aligned}
 (\text{Ntr 1.2}) \quad \llbracket == \rrbracket(u, w) &= \text{tt} && \text{if } u = w \\
 &= \text{ff} && \text{otherwise.}
 \end{aligned}$$

$$(\text{Pos}) \quad \llbracket \text{suc}_d \rrbracket(b) = d.b$$

$$(\text{Pol}) \quad \llbracket \text{calloc} \rrbracket(u, w) = \underbrace{w \dots w}_{|u| \text{ times}}$$

If Δ is a safe operator typing environment then :

$$\Delta(\text{pred}) \subseteq \{\mathbf{0} \rightarrow \mathbf{0}, \mathbf{1} \rightarrow \mathbf{1}, \mathbf{1} \rightarrow \mathbf{0}\},$$

$$\Delta(==) \subseteq \{\mathbf{1} \rightarrow \mathbf{1} \rightarrow \mathbf{1}, \alpha \rightarrow \beta \rightarrow \mathbf{0}, \alpha, \beta \in \{\mathbf{0}, \mathbf{1}\}\},$$

$$\Delta(\text{suc}_d) \subseteq \{\mathbf{1} \rightarrow \mathbf{0}, \mathbf{0} \rightarrow \mathbf{0}\},$$

$$\Delta(\text{calloc}) = \{\mathbf{1} \rightarrow \mathbf{1} \rightarrow \mathbf{0}\}.$$

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Definition (Safe process)

Given Γ a variable typing environment and Δ a operator typing environment, a process P is a *safe process* if :

- ▶ P is well-typed wrt Γ and Δ , i.e. $\Gamma, \Delta \vdash P : \beta$
- ▶ and Δ is safe

The search(str) program is safe wrt Γ, Δ provided.

Polynomial space

Theorem

The set of Pspace decision problems is exactly the set of problems decided by :

- ▶ *safe,*
- ▶ *confluent,*
- ▶ *strongly normalizing,*
- ▶ *and lock-free processes P*

Corollary

If P is a safe, confluent, strongly normalizing and lock-free processes P such that $\Delta, \Gamma \vdash P : \mathbf{1}$ then the function computed by P is in FPspace.

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Intermediate Lemmata on tier **1** values

Lemma (Simple security)

Given a safe process P wrt Γ and Δ , if $\Gamma, \Delta \vdash E : \mathbf{1}$ then $\forall X \in \mathcal{V}(E), \Gamma(X) = \mathbf{1}$ and all operators in E are neutral.

Lemma (Bounded size)

Given a safe process P wrt Γ and Δ s.t. $\Gamma, \Delta \vdash E : \mathbf{1}$, for each store μ , if $\forall X \in \mathcal{V}(E), \mu(X) \in \mathbb{W}_n$ and $(E, \mu) \xrightarrow{e} d$ then $d \in \mathbb{W}_n$.

Lemma (Bounded cardinality)

Given a safe process P wrt Γ and Δ and $\Gamma, \Delta \vdash E : \mathbf{1}$, the number of distinct values taken by E during the evaluation of $(P, \mu)_\emptyset$ is bounded polynomially in $|\mu|$.

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Process tree

Definition (Process tree)

The process tree $T(\mathcal{E})$ of an environment \mathcal{E} is defined by :

- ▶ the nodes are the configurations $\{\mathcal{E}_1, \dots, \mathcal{E}_{\#\mathcal{E}}\}$
- ▶ the root is \mathcal{E}_1 ;
- ▶ for each $l \in [1, \#\mathcal{E}]$, there is an edge from $\mathcal{E}_l = (P, \mu)_\rho$ to \mathcal{E}_k , if $k \in \rho$.

Given a process tree T , its degree is denoted $d(T)$ and height $h(T)$.

\rightsquigarrow pstree

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Intermediate Lemmata on the process tree

Lemma (Bounded degree)

Given a strongly normalizing and safe process P , there exists a polynomial Q s.t., $\forall \mu$, if $(P, \mu)_\emptyset \rightarrow^* \mathcal{E}$ then $d(T(\mathcal{E})) \leq Q(|\mu|)$.

Lemma (Bounded height)

Given a strongly normalizing and safe process P , there exists a polynomial Q s.t., $\forall \mu$, if $(P, \mu)_\emptyset \rightarrow^* \mathcal{E}$ then $h(T(\mathcal{E})) \leq Q(|\mu|)$.

Lemma (Subprocesses in polynomial time)

Given a strongly normalizing and safe process P , there is a polynomial Q s.t., $\forall \mu$ and $\forall i \in \mathbb{N}$, if $(P, \mu)_\emptyset \Rightarrow_i^k \mathcal{E}$ then $k \leq Q(|\mu|)$.

where \Rightarrow_i^k means that the i -th configuration has been evaluated k times.

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Pspace abiding strategy

Lemma (Bounded stores)

Given a strongly normalizing and safe decision process P , there exists a polynomial Q such that, $\forall \mu$, if $(P, \mu)_\emptyset \rightarrow^* \mathcal{E}$ then $\forall i \leq \# \mathcal{E}$, if $\mathcal{E}_i = (P_i, \mu_i)_{\rho_i}$ then $|\mu_i| \leq Q(|\mu|)$.

Soundness.

We define a lazy Pspace abiding evaluation strategy that :

Init defines the current process to be the main process configuration

- ▶ executes the current process as long as possible
- ▶ on a wait instruction updates the current process to the waited process
- ▶ on a return instruction updates the current process to the father



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We write a safe process computing QBF :

- ▶ when an $\exists x (\forall)$ is encountered a fork instruction is called :
 - ▶ the son evaluates the remaining formula with x set to \top
 - ▶ whereas the father evaluates the remaining formula with x set to \perp
 - ▶ at the end the father gets his son's result and computes the disjunction with its own result (conjunction)
- ▶ A calloc operator is used in order to store the processes id.

Conclusions

- ▶ We have a characterization of Pspace combining non-interference and tiering methodologies
- ▶ The system is expressive (very close to C fork programs or Unix processes)
- ▶ It allows the programmer to simulate malloc/calloc operators (Polynomial operators)
- ▶ Possible extension on threads with creation

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