Sorin Stratulat

Associate Professor

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Curriculum Vitae

I. IDENTITY

Family Name	Stratulat
First Name	Sorin
Birthday	May 09, 1971
Nationalities	French, Romanian
Current Position	Associate Professor in Computer Science
Place	UFR-MIM at University of Lorraine (UL) – Metz
Research Lab	LORIA (Laboratoire lorrain de recherche en informatique et ses applications). Member of MOSEL/VeriDis team.
Diplomas	Habilitation in Computer Science, UL, June 29, 2021 PhD in Computer Science, University 'Henri Poincaré' (UHP)-Nancy I (1996-2000) Master in Computer Science, UHP-Nancy I (1995-1996) 'Licență în informatică' (Bac+5), Polytech. Univ. of Bucharest, 1990-1995, Romania

Positions

since 2003	Associate Professor, UL – Metz
2018-2019	On leave for full-time research at INRIA Grand Est
2001-2003	Postdocs: SRI-International (3 months, Menlo Park, USA), DIST (5 mois, Genova, Italy), INRIA (1 year, Sophia Antipolis)

Main Research Interests and Tools

First-order logic with inductive definitions and equality
Theorem proving using induction techniques, Noetherian induction reasoning
Cooperation and integration of reasoning systems, decision procedures
Specification and test of critical software
Algorithm synthesis
Proof certification, Coq
Spike, E-Cyclist

II. Publication List

Book : 1

[1] S. Stratulat : *Preuves par récurrence avec ensembles couvrants contextuels. Applications à la vérification de logiciels de télécommunications.* Editions Universitaires Européennes, 2012. ISBN 978-3841794901. re-édition de la thèse de doctorat.

International Journals : 6

[2] I. Dramnesc, T. Jebelean, and S. Stratulat. Mechanical synthesis of sorting algorithms for binary trees by logic and combinatorial techniques. *Journal of Symbolic Computation*, 90:3–41, 2019.

[3] S. Stratulat. Mechanically certifying formula-based Noetherian induction reasoning. *Journal of Symbolic Computation*, 80, Part 1:209–249, 2017.

[4] M. Rusinowitch, S. Stratulat, and F. Klay. Mechanical verification of an ideal incremental ABR conformance algorithm. *Journal of Automated Reasoning*, 30(2):153–177, 2003.

[5] A. Armando, M. Rusinowitch, and S. Stratulat. Incorporating decision procedures in implicit induction. *Journal of Symbolic Computation*, 34(4):241–258, 2002.

[6] S. Stratulat. A general framework to build contextual cover set induction provers. *Journal of Symbolic Computation*, 32(4):403–445, 2001.

[7] S. Stratulat and D. J. Evans. Virtual shared memory machines - an application of PVM. *Parallel Algorithms Appl.*, 7(1-2):143–160, 1995.

National Journals : 3

[8] S. Stratulat. Récurrence noethérienne pour le raisonnement de premier ordre. *1024 – Bulletin de la Société informatique de France*, (19):157–169, 2022.

[9] C. Popa, I. Lopatan, and S. Stratulat. Automated problem solving and argumentation. *Revue Roumaine de Sciences Juridiques*, 8(1):97–108, 1997.

[10] C. Popa, I. Lopatan, and S. Stratulat. Logica, prologul si hotararea judecatoreasca. *Revista de drept romanesc (Revue de droit roumain)*, 6(4):373–389, 1994.

International Conferences : 31

[11] S. Stratulat. E-Cyclist: Implementation of an efficient validation of $\mathrm{FOL}_{\mathrm{ID}}$ cyclic induction reasoning. In T. Kutsia, editor, *9th International Symposium on Symbolic Computation in Software Science*, volume 342 of *Electronic Proceedings in Theoretical Computer Science*, pages 129–135, June 2021.

[12] S. Stratulat. SPIKE, an automatic theorem prover – revisited. In *SYNASC 2020: Proceedings of the 22nd International Symposium on Symbolic and Numeric Algorithms for Scientific Computing*, pages 93–96. IEEE Computer Society, 2020.

[13] S. Stratulat. Validating back-links of $\mathsf{FOL}_{\mathsf{ID}}$ cyclic pre-proofs. In S. Berardi and S. van Bakel, editors, *CL&C'18 (Seventh International Workshop on Classical Logic and Computation)*, number 281 in EPTCS, pages 39–53, 2018.

[14] S. Stratulat. Cyclic proofs with ordering constraints. In R. A. Schmidt and C. Nalon, editors, *TABLEAUX 2017 (26th International Conference on Automated Reasoning with Analytic Tableaux and Related Methods)*, volume 10501 of *LNAI*, pages 311–327. Springer, 2017.

[15] S. Stratulat. Structural vs. cyclic induction: a report on some experiments with Coq. In *SYNASC (International Symposium on Symbolic and Numeric Algorithms for Scientific Computing)*, pages 27–34. IEEE Computer Society, 2016.

[16] I. Dramnesc, T. Jebelean, and S. Stratulat. Proof-based synthesis of sorting algorithms for trees. In *LATA 2016: 10th International Conference on Language and Automata Theory and Applications*, volume 9618 of *Lecture Notes Computer Science*, pages 562–575. Springer Verlag, 2016.

[17] I. Dramnesc, T. Jebelean, and S. Stratulat. A case study on algorithm discovery from proofs: The insert function on binary trees. In *SACI 2016: 11th IEEE International Symposium on Applied Computational Intelligence and Informatics*, pages 231–236. IEEE, 2016.

[18] I. Dramnesc, T. Jebelean, and S. Stratulat. Theory exploration of binary trees. In *13th IEEE International Symposium on Intelligent Systems and Informatics (SISY 2015)*, pages 139–144. IEEE Publishing, 2015.

[19] I. Dramnesc, T. Jebelean, and S. Stratulat. Combinatorial techniques for proof-based synthesis of sorting algorithms. In *SYNASC 2015: Proceedings of the 17th International Symposium on Symbolic and Numeric Algorithms for Scientific Computing*, pages 137–144. IEEE Computer Society, 2015.

[20] G. Michel, V. Jeanclaude, and S. Stratulat. Better careers for transnational European students ? In Á. Herrero, B. Baruque, J. Sedano, H. Quintián, and E. Corchado, editors, *ICEUTE'2015 (Sixth International Conference on EUropean Transnational Education)*, volume 369 of *Advances in Intelligent Systems and Computing*, pages 517–524. Springer, 2015.

[21] S. Stratulat. Implementing reasoning modules in implicit induction theorem provers. In *SYNASC (International Symposium on Symbolic and Numeric Algorithms for Scientific Computing)*, pages 133–140. IEEE Computer Society, 2014.

[22] T. Aoto and S. Stratulat. Decision procedures for proving inductive theorems without induction. In *Proc. of the 16th International Symposium on Principles and Practice of Declarative Programming (PPDP 2014)*, pages 237–248. ACM Press, 2014.

[23] A. Henaien and S. Stratulat. Performing implicit induction reasoning with certifying proof environments. In A. Bouhoula, T. Ida, and F. Kamareddine, editors, *Proceedings Fourth International Symposium on Symbolic Computation in Software Science,*

Gammarth, Tunisia, 15-17 December 2012, volume 122 of *Electronic Proceedings in Theoretical Computer Science*, pages 97–108. Open Publishing Association, 2013.

[24] S. Stratulat. A unified view of induction reasoning for first-order logic. In A. Voronkov, editor, *Turing-100 (The Alan Turing Centenary Conference)*, volume 10 of *EPiC Series*, pages 326–352. EasyChair, 2012.

[25] S. Stratulat and V. Demange. Automated certification of implicit induction proofs. In *CPP'2011 (First International Conference on Certified Programs and Proofs)*, volume 7086 of *Lecture Notes Computer Science*, pages 37–53. Springer Verlag, 2011.

[26] G. Michel and S. Stratulat. Creating transnational courses: Feedback and solutions. In *7th International Conference on Next Generation Web Services Practices (NWeSP)*, pages 481–486, oct. 2011.

[27] S. Stratulat. Integrating implicit induction proofs into certified proof environments. In *IFM'2010 (8th International Conference on Integrated Formal Methods)*, volume 6396 of *Lecture Notes in Computer Science*, pages 320–335, 2010.

[28] G. Michel and S. Stratulat. Good reasons to implement transnational European diploma programs in Computer Science. In *ICEUTE'2010 (First International Conference on EUropean Transnational Education)*, pages 135–143, 2010.

[29] S. Stratulat. Combining rewriting with Noetherian induction to reason on non-orientable equalities. In A. Voronkov, editor, *Rewriting Techniques and Applications*, volume 5117 of *Lecture Notes in Computer Science*, pages 351–365. Springer Berlin, 2008.

[30] S. Stratulat. 'Descente Infinie' induction-based saturation procedures. In *SYNASC '07: Proceedings of the Ninth International Symposium on Symbolic and Numeric Algorithms for Scientific Computing*, pages 17–24, Washington, DC, USA, 2007. IEEE Computer Society.

[31] S. Stratulat. Automatic 'Descente Infinie' induction reasoning. In B. Beckert, editor, *TABLEAUX*, volume 3702 of *Lecture Notes in Artificial Intelligence*, pages 262–276. Springer, 2005.

[32] G. Barthe and S. Stratulat. Validation of the JavaCard platform with implicit induction techniques. In R. Nieuwenhuis, editor, *RTA (Rewriting Techniques and Applications)*, volume 2706 of *LNCS*, pages 337–351. Springer, 2003.

[33] A. Imine, Y. Slimani, and S. Stratulat. Inductive theorem prover based verification of concurrent algorithms. In *MCSEAI02 (7th Maghrebian Conference on Computer Science)*, pages 313–324, 2002.

[34] A. Imine, Y. Slimani, and S. Stratulat. Using automated induction-based theorem provers for reasoning about concurrent systems. In Michel Rueher, editor, *Onzièmes Journées Francophones de Programmation Logique et Programmation par Contraintes (JFPLC)*, pages 71–85, 2002.

[35] A. Armando, M. Rusinowitch, and S. Stratulat. Incorporating decision procedures in implicit induction. In *Calculemus 2001 (9th Symposium on the Integration of Symbolic Computation and Mechanized Reasoning)*, pages 142–155, 2001.

[36] M. Rusinowitch, S. Stratulat, and F. Klay. Mechanical verification of an ideal incremental ABR conformance algorithm. In E. A. Emerson and A. Prasad Sistla, editors, *CAV*, volume 1855 of *Lecture Notes in Computer Science*, pages 344–357. Springer, 2000.

[37] F. Klay, M. Rusinowitch, and S. Stratulat. Analysing feature interactions with automated deduction systems. In *ICOTS (7th International Conference on Telecommunication Systems, Modelling and Analysis)*, pages 542–554, 1999.

[38] M. Rusinowitch, S. Stratulat, and F. Klay. Mechanical verification of a generic incremental ABR conformance algorithm. In F. Bellegarde and O. Kouchnarenko, editors, *Workshop on Modelling and Verification*, Besançon 1999.

[39] S. Stratulat. Applying semantic subsumption rules in the context of inductive proofs. In *Workshop on Integration of Deductive Systems, CADE-15, Lindau*, pages 85–95, 1998.

[40] S. Stratulat. SPIKEpar : une interface parallèle du démonstrateur automatique SPIKE. In *Dixièmes Rencontres Francophones du Parallélisme (RenPar'10)*, pages 209–212, 1998.

[41] C. Popa, I. Lopatan, and S. Stratulat. Logic, Prolog and legal sentence. In *Workshop on Legal Applications of Logic Programming, Genova*, pages 112–127, 1994.

Software : 2

[42] *Le démonstrateur de théorèmes SPIKE*, 1997-2022. `https://github.com/sorinica/spike-prover`.

[43] S. Stratulat. The cyclic prover E-Cyclist. `https://members.loria.fr/SStratulat/files/e-cyclist.zip`, 2021.

Technical Reports : 4

[44] I. Dramnesc, T. Jebelean, and S. Stratulat. Synthesis of some algorithms for trees: Experiments in Theorema. Technical Report 15-04, Johannes Kepler University, Linz, Austria, 2015.

[45] A. Armando, G. Défourneaux, M. Rusinowitch, and S. Stratulat. Integrating decision procedures in SPIKE. Technical Report 37, LORIA, 1999.

[46] S. Stratulat. Integration of reasoning modules in a rewrite-based theorem prover. Technical Report 135, CRIN, 1997.

[47] groupe PROTHEO. Rapports trimestriels dans le cadre du contrat 96 1B 008 avec le CNET, 1996-1997.

Theses : 4

[48] S. Stratulat. Noetherian induction for computer-assisted first-order reasoning. Mémoire d'habilitation en informatique, Université de Lorraine. `https://hal.archives-ouvertes.fr/tel-03286314`, juin 2021.

[49] S. Stratulat. *Preuves par récurrence avec ensembles couvrants contextuels. Applications à la vérification de logiciels de télécommunications.* PhD thesis, Université Henri Poincaré, Nancy I, November 2000.

[50] S. Stratulat. Vérification automatisée de spécifications de services téléphoniques. Master's thesis, Université Henri Poincaré - Nancy I, 1996.

[51] S. Stratulat. Virtual shared memory machines and how to write software for them. Rapport de fin d'études pour obtenir le titre de 'Licencié'/Ingénieur en informatique, 1995.

Contributions

- proof by induction techniques: [8, 24, 30]
- implicit induction: [12, 1, 5, 6, 21, 29, 31, 35, 39, 45]
- cyclic induction: [11, 13, 14, 15]
- complementary reasoning techniques: [22]
- proof certification: [3, 23, 25, 27]
- implementation of implicit induction provers: [40, 42, 46]
- validation of software for smartcards: [32]
- validation of telecommunications protocols and services: [4, 36, 37, 38, 47]
- formal design of concurrent algorithms: [33, 34]
- algorithm synthesis: [2, 16, 17, 18, 19, 44]
- parallel calculus: [7]
- logic for law: [9, 10, 41]

III. Other Research Activities

Supervision of PhD Students: 2

- HENAIEN Amira. Certification des raisonnements formels portant sur des systèmes d'information critiques, 2010-2015
- DEMANGE Vincent. Vers un calcul de constructions pédagogique, 2008-2012

Supervision of Master Students: 4

- HENAIEN Amira. Certification des preuves par saturation, 2009-2010
- VIENOT Camille. *Checking Simultaneously Completeness and Ground Confluence for Algebraic Specifications using SPIKE*, (en collaboration avec l'Ecole des Mines, Nancy), 2009-2010
- BENKADDOUR Mohamed. Etude et implementation de techniques de preuves par saturation dans des démonstrateurs de théorèmes basés sur la récurrence de type 'Descente Infinie', 2008-2009
- KALISZYK Cezary. Validation des preuves par récurrence implicite avec des outils basés sur le calcul des constructions inductives, 2004-2005

PC Member

- CISIS (Conference on Computational Intelligence in Security for Information Systems): 2008 – 2022, IAS (International Conference on Information Assurance and Security): 2010 – 2014, 2018 – 2020, ICEUTE (International Conference on EUropean Transnational Education): 2013, 2015, 2016, 2017, 2020, 2021, SCSS (International Symposium on Symbolic Computation in Software Science): 2013, SYNASC: 2008 – 2012, 2014 – 2021, FROM (Working Formal Methods Symposium): 2019 – 2020

Reviewer Activities

- as PC member (see above the list of conferences)
- Journal of Logical and Algebraic Methods in Programming 2020, FSCD'17 (Second International Conference on Formal Structures for Computation and Deduction), CSL 2017 (26th EACSL Annual Conference on Computer Science Logic), CSL 2021, FoSSaCS 2017 (20th International Conference on Foundations of Software Science and Computation Structures), Mathematical Reviews: 2016, SCSS 2015, CADE 2015 (25th International Conference on Automated Deduction), TACAS 2015 (21st International Conference on Tools and Algorithms for the Construction and Analysis of Systems), FRoCoS 2015 (10th International Symposium on Frontiers of Combining Systems), IJCAR 2012 (6th International Joint Conference on Automated Reasoning), JSC (Journal of Symbolic Computation) - Special issues of Symbolic Computation in Software Science: 2009, 2010, 2013, TOCL (ACM Transactions on Computational Logic): 2007, IPL (Information Processing Letters): 2006, RTA 2006

Participation to National and Regional Projects

- ANR **ARROWS** (Safe Pointer-based Data Structures: A Declarative Approach to Their Specification and Analysis). Participants: CASSIS (LORIA-Nancy), CAPP (LEIBNIZ-Grenoble), LILaC (IRIT-Toulouse). Membre invité de l'équipe CASSIS, 2005-2008.
- *ACI Sécurité* **GECCOO** (Generation of Certified Code for Object Oriented Applications) `http://geccoo.lri.fr/`. Participants : TFC (LIFC-Besançon), CASSIS (LORIA-Nancy), EVEREST (INRIA-Sophia Antipolis), ProVal (LRI-Paris), VASCO (LSR-Grenoble). Invited member, 2003-

2006.

- **Certicartes** as part of the European project Verificard `http://www-sop.inria.fr/lemme/verificard/`. Participants: LogiCal (INRIA-Roquencourt), Lande (INRIA-Rennes/IRISA), Lemme (INRIA-Sophia Antipolis). Invited member, 2002-2003.
- **PSW** (Preuves de Services Web). (`https://sites.google.com/site/preuvesdeservicesweb/journee-thematique`). Participants: CMI (LITA – Metz), CASSIS (LORIA – Nancy), ECCOO (LORIA – Nancy), 2007 – 2008.

Présentations as Invited Speaker

- *Efficient Validation of FOL_{ID} Cyclic Induction Reasoning*. Séminaire Dagstuhl numéro 19371 en Allemagne, 10 septembre 2019, 1/2 h
- *Efficient Validation of FOL_{ID} Cyclic Induction Reasoning*. Working Formal Methods Symposium 2019, Timisoara, Roumanie, le 04 septembre 2019, 1/2 h. The complete list of invited speakers is available at `https://from2019.projects.uvt.ro/invited-speakers/`
- *Cyclic proofs with ordering constraints*, séminaire Dagstuhl numéro 17371 en Allemagne, 11 septembre 2017, 1/2 h
- *Affordable Cyclic Noetherian Induction Reasoning*, Journées GEOCAL-LAC-LTP, LORIA-Nancy, 12 octobre 2015, 1/2 h
- *Mechanizing induction reasoning for first-order logic with inductive definitions*. Séminaire de recherche des départements d'informatique et de mathématiques de l'Université de l'Ouest de Timisoara, Roumanie, le 12 avril 2017, 1 h
- *Mechanically certifying formula-based Noetherian induction reasoning*. SYNASC'2016 (18th International Symposium on Symbolic and Numeric Algorithms for Scientific Computing), Timisoara, Roumanie, 25 septembre 2016, 1 h
- *Noetherian induction for first-order logic*. PAS'2012 (International Seminar on Program Verification, Automated Debugging and Symbolic Computation), Pékin, Chine, 10 octobre 2012, 1 h. La liste complète des conférenciers invités à cet évènement, dont des chercheurs reconnus au niveau international pour leurs contributions dans les domaines du calcul symbolique et de la vérification de logiciels, est disponible à `http://pas2012.cc4cm.org/`
- *Automated certification of implicit induction proofs*. Workshop on Formal methods for specifying and verifying critical systems, Technologic Park El Ghazala - Tunis, Tunisie, 7 décembre 2011, 1/2 h
- *Certifying implicit induction proofs*, Journées GEOCAL-LAC, LORIA-Nancy, 12 mai 2011, 1 h
- *Induction Proof Techniques to Reason on Non-Orientable Equalities*. Séminaire de recherche des départements d'Informatique et des Mathématiques, 15 avril 2010, Université de Pitesti, Roumanie, 1 h
- *Integrating implicit induction proofs into certified proof environments*. Séminaire de recherche de l'équipe PROVAL, Orsay, 5 octobre 2010, 1 h
- *Automatic 'Descente Infinie' Induction Reasoning*. WACI'2007 (2nd Workshop on Applications of Computational Intelligence), Coimbra, Portugal, 5 décembre 2007, 1/2h. Aussi présentée aux séminaires de recherche des départements d'Informatique et des Mathématiques de 'Babes-Bolyai' Cluj-Napoca, Roumanie (03 mai 2011), de l'Université de Pitesti, Roumanie (24 avril 2008), de l'Université de l'Ouest de Timisoara, Roumanie (11 avril 2007), et de l'Université 'Alexandru-Ioan Cuza' de Iasi, Roumanie (5 mai 2006)

- *'Descente Infinie' Theorem Proving*. Séminaire de recherche de DFKI, Saarbruck, Allemagne, 22 février 2005, 1 h
- *Validation of the JavaCard platform with implicit induction techniques*, Séminaire du *Faculty of Information Sciences* de l'Université de Tampere, Finlande, mai 2004, 1 h

Other Presentations and Tutorials

- *E-Cyclist : Implementation of an efficient validation of FOL_{ID} cyclic induction reasoning*, SCSS (International Symposium on Symbolic Computation in Software Science), Linz, Autriche, le 09 septembre 2021, 1/2 h (remotely).
- *SPIKE, an automatic theorem prover – revisited*, SYNASC 2020, Timisoara, Roumanie, 1er septembre 2020, 1/3 h (remotely)
- *Efficient validation of FOL_{ID} cyclic induction reasoning*, CiSS 2019 (Circularity in Syntax and Semantics), Gothenburg, Suède, 22 novembre 2019, 1/2 h
- *Useless Explicit Induction Reasoning*, PARIS 2018 (Programming And Reasoning on Infinite Structures), Oxford, Angleterre, 7 juillet 2018, 1/2 h
- *Validating back-links in FOL_{ID} cyclic proofs*, CL&C 2018 (International Workshop on Classical Logic and Computation), Oxford, Angleterre, 7 juillet 2018, 1/2 h. Aussi présentée à SYNASC 2017, Timisoara, Roumanie, 21 septembre 2017
- *Mechanically Certifying Formula-based Noetherian Induction Reasoning*, WAIT 2018 (4th International Workshop on Automated (Co)inductive Theorem Proving), Amsterdam, Pays-Bas, 1/2 h, 28 juin 2018. Aussi présentée à SYNASC 2016, Timisoara, Roumanie, 25 septembre 2016
- *Cyclic proofs with ordering constraints*, TABLEAUX 2017 (Analytic Tableaux and Related Methods), Brasília, Brésil, 26 septembre 2017, 1/2 h. Egalement présentée 'Workshop on Termination and Circular Proofs', Le Bourget-du-Lac, 19 juillet 2017
- *Affordable Cyclic Noetherian Induction Reasoning*, Journées GEOCAL-LAC-LTP, LORIA-Nancy, 12 octobre 2015, 1/2 h
- *Implementing Reasoning Modules in Implicit Induction Theorem Provers*, SYNASC 2014, Timisoara, Roumanie, 22 septembre 2014, 1/2 h
- *Decision procedures for proving inductive theorems without induction*, PPDP 2014 (6th International Symposium on Principles and Practice of Declarative Programming), Canterbury, Grande-Bretagne, 10 septembre 2014, 1/2 h
- *Building explicit induction schemas for cyclic induction reasoning*, PAS 2014 (3rd International Seminar on Program Verification, Automated Debugging and Symbolic Computation) dans le cadre de *Vienna Summer of Logic*, Vienne, Autriche, 18 juillet 2014, 1/2 h
- *A unified view of induction reasoning for first-order logic*, Turing-100 (The Alan Turing Centenary Conference), Manchester, Grande-Bretagne, 24 juin 2012. Poster session.
- *Certifying formula-based induction reasoning*, PSATTT (International Workshop on Proof-Search in Axiomatic Theories and Type Theories), LIX, Palaiseau, France, 08 novembre 2013, 1/2 h
- *Reductive-free cyclic induction reasoning*, TAFP (Theory and Application of Formal Proofs), LIX, Palaiseau, France, 05 novembre 2013, 1/2 h
- *Making explicit the implicit induction*, WITP (Workshop on Inductive Theorem Proving), Imperial College, Londres, Grande-Bretagne, le 23 novembre 2013, 1 h
- *Noetherian Induction for First-Order Reasoning*, tutoriel pour TABLEAUX'2013, Nancy, France, 18 septembre 2013, 2 hs, `http://tableaux13.loria.fr/tuto1.html`

- *Integrating implicit induction proofs into certified proof environments*, IFM'2010 (Integrated Formal Methods), LORIA-Nancy, 14 octobre 2010, 1/2 h
- *Validating implicit induction proofs using certified proof environments*, Grande Region Security and Reliability Day, 18 mars 2010. Poster session.
- *Combining rewriting with Noetherian induction to reason on non-orientable equalities*, RTA'2008 (Rewriting Techniques and Applications), Hagenberg, Autriche, 17 juillet 2008, 1/2 h
- *Descente Infinie' induction-based saturation procedures*, SYNASC'2007, Timisoara, Roumanie, 26 septembre 2007, 1/2 h
- *Automatic 'Descente Infinie' induction reasoning*. TABLEAUX'2005, Coblence, Allemagne, 15 septembre 2005, 1/2 h
- *Validation of the JavaCard platform with implicit induction techniques*. RTA'2003 (Rewriting Techniques and Applications), Valence, Espagne, 11 juin 2003, 1/2 h

Certificate for *best paper award*